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PTO/SB/08A (08-03)

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Application Number	10/689,355
Filing Date	20 October 2003
First Named Inventor	Mark Beaumont
Art Unit	Not yet assigned
Examiner Name	Not yet assigned
Attorney Docket Number	DB001061-000

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature	/Eric Wai/	Date Considered	07/20/2007
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STATEMENT BY APPLICANT**

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U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
/EW/		US- 6,430,618 B1	08-06-2002	Karger et al.	
/EW/		US- 6,421,772 B1	07-16-2002	Maeda et al.	
/EW/		US- 6,404,439 B1	06-11-2002	Coulombe et al.	
/EW/		US- 6,292,822 B1	09-18-2001	Hardwick	
/EW/		US- 6,219,776 B1	04-17-2001	Pechanek et al.	
/EW/		US- 6,279,088 B1	08-21-2001	Elliot et al.	
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/EW/		US- 5,966,528	10-12-1999	Wilkinson et al.	
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/EW/		US- 5,850,489	12-15-1998	Rich	
/EW/		US- 5,630,129	05-13-1997	Wheat	
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/EW/		US- 5,535,387	07-09-1996	Matsouka et al.	
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/EW/		Daehyun Kim, Mainak Chaudhuri, and Mark Heinrich, Leveraging Cache Coherence in Active Memory Systems, Proceedings of the 16th ACM	
		Int'l Conference on Supercomputing, pages 2-13, New York City, USA, June 2002.	
/EW/		Mainak Chaudhuri, Daehyun Kim, and Mark Heinrich, Cache Coherence Protocol Design for Active Memory Systems, Proceedings of the 2002	
		Int'l Conference on Parallel and Distributed Processing Techniques and Applications, pages 83-89, Las Vegas, USA, June 2002.	

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